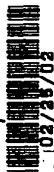


31000 U.S. PTO
10/080710



02/25/02

15c1120

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NO.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10080710	02/25/2002	365		2818	P. 57

**APPLICANTS: Park Sang; Mun Joo; Kwon Hyeok

**CONTINUING DATA VERIFIED: *no*

** FOREIGN APPLICATIONS VERIFIED: *yes LP*
REPUBLIC OF KOREA 2001-10097 02/27/2001

PG-PUB DO NOT PUBLISH ☐ RESCIND ☐

Foreign priority claimed: ☒ yes ☐ no
35 USC 110 condition: ☒ yes ☐ no
Verified and Acknowledged: Examiner's Initials: *LP*

ATTORNEY DOCKET NO. F 204-10080710-0212002

TITLE: System of performing a repair analysis for a semiconductor memory device having a redundant architecture

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)